

100V N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ◇ Advanced SGT cell design
- ◇ Low Thermal Resistance

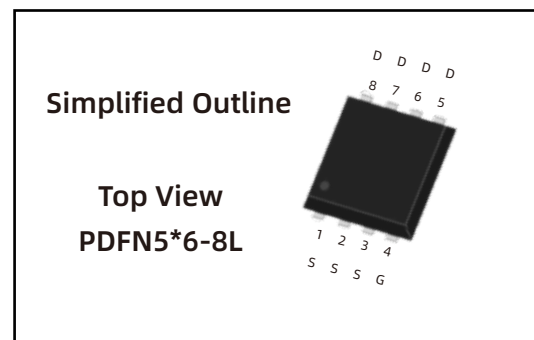
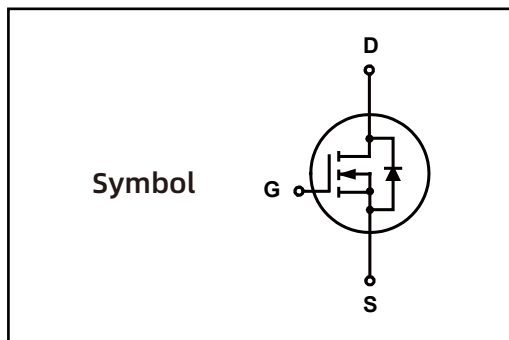
1.2 Applications

- ◇ Motor drivers
- ◇ DC-DC Converter

1.3 Quick reference

- ◇ $BV \cong 100\text{ V}$
- ◇ $P_{\text{tot}} \cong 96\text{ W}$
- ◇ $I_D \cong 85\text{ A}$
- ◇ $R_{\text{DS(ON)}} \cong 6.8\text{ m}\Omega @ V_{\text{GS}} = 10\text{ V}$

2. Pin Description



3. Marking Information

Product Name	Marking
LN060N100G-H	LN060N100G-H CYWWZZ XXXXXX

4. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C = 25\text{ }^\circ\text{C}$	-	100	V
V_{GS}	Gate-Source Voltage	$T_C = 25\text{ }^\circ\text{C}$	-	± 20	V
I_D^*	Drain Current (DC)	$T_C = 25\text{ }^\circ\text{C}, V_{GS} = 10\text{ V}$	-	85	A
		$T_C = 100\text{ }^\circ\text{C}, V_{GS} = 10\text{ V}$	-	52	A
$I_{DM}^{*,***}$	Drain Current (Pulsed)	$T_C = 25\text{ }^\circ\text{C}, V_{GS} = 10\text{ V}$	-	320	A
P_{tot}^*	Total power dissipation	$T_C = 25\text{ }^\circ\text{C}$	-	96	W
T_{stg}	Storage Temperature		- 55	150	$^\circ\text{C}$
T_J	Junction Temperature		-	150	$^\circ\text{C}$
I_S	Diode Forward Current	$T_C = 25\text{ }^\circ\text{C}$	-	85	A
E_{AS}^*	Single Pulsed Avalanche Energy	$V_{DD} = 50\text{ V}, L = 1\text{ mH}$	-	220	mJ
$R_{\theta JA}^*$	Thermal Resistance- Junction to Ambient		-	42.6	$^\circ\text{C/W}$
$R_{\theta JC}^*$	Thermal Resistance- Junction to Case		-	1.3	

Notes :

- * Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$
- ** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
- *** Limited by bonding wire

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
LN060N100G-H	PDFN5*6			5000	

Note: COMTECH defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics ($T_A=25^\circ$ Unless Otherwise Noted)

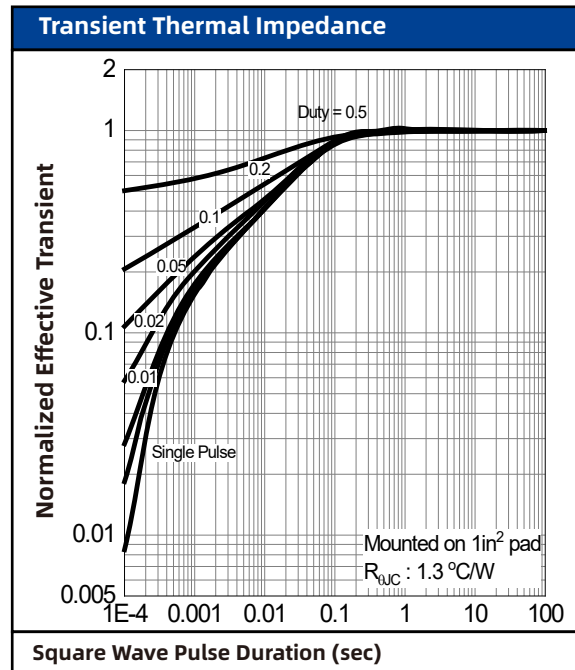
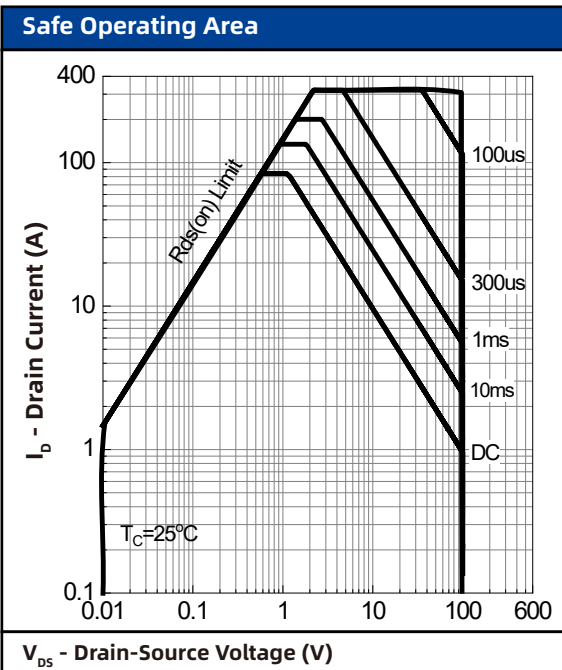
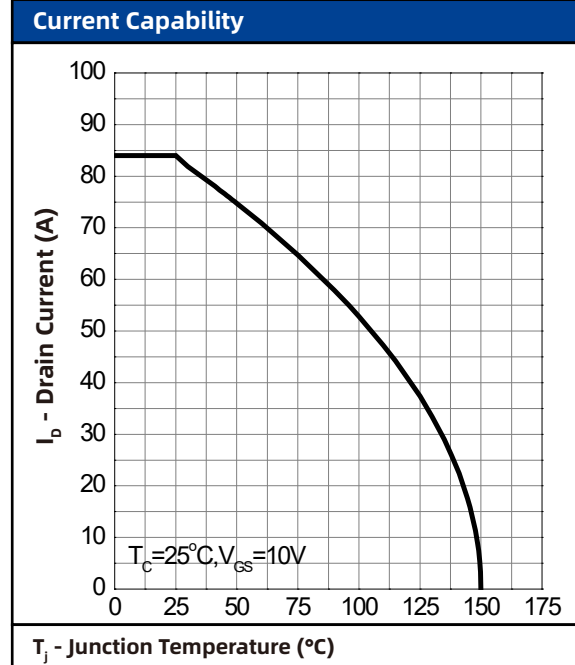
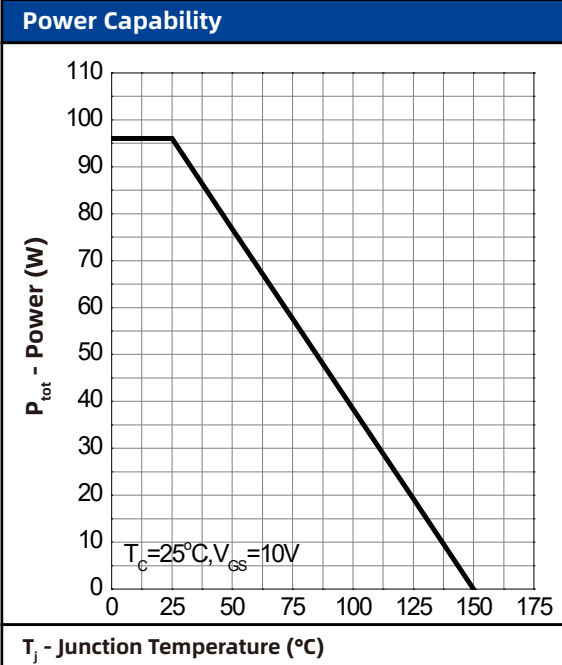
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	100	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{DS} = 250\ \mu\text{A}$	2	-	4	V
I_{DSS}	Zero Gate Voltage Source Current	$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
I_{GSS}	Gate Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	-	-	± 100	nA
$R_{DS(ON)}^a$	Drain-Source On-State Resistance	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	5.8	6.8	m Ω
Diode Characteristics						
V_{SD}^a	Diode Forward Voltage	$I_{SD} = 20\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.3	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 20\text{ A}$	-	70	-	nS
Q_{rr}	Reverse Recovery Charge	$dI_{SD}/dt = 100\text{ A}/\mu\text{s}$	-	91	-	nC
Dynamic Characteristics^b						
C_{ISS}	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = 50\text{ V}$ Frequency = 1 MHz	-	3152	-	pF
C_{OSS}	Output Capacitance		-	841	-	
C_{rSS}	Reverse Transfer Capacitance		-	8	-	
$t_d(on)$	Turn-on Delay Time	$V_{DS} = 50\text{ V}, V_{GEN} = 10\text{ V},$ $R_G = 3.9\ \Omega, R_L = 2.5\ \Omega,$ $I_{DS} = 20\text{ A}$	-	17	-	nS
t_r	Turn-on Rise Time		-	12	-	
$t_d(off)$	Turn-off Delay Time		-	35	-	
t_f	Turn-off Fall Time		-	11	-	
Gate Charge Characteristics^b						
Q_g	Total Gate Charge	$V_{DS} = 50\text{ V}, V_{GS} = 10\text{ V},$ $I_{DS} = 20\text{ A}$	-	55	-	nC
Q_{gs}	Gate-Source Charge		-	19	-	
Q_{gd}	Gate-Drain Charge		-	14	-	

Notes :

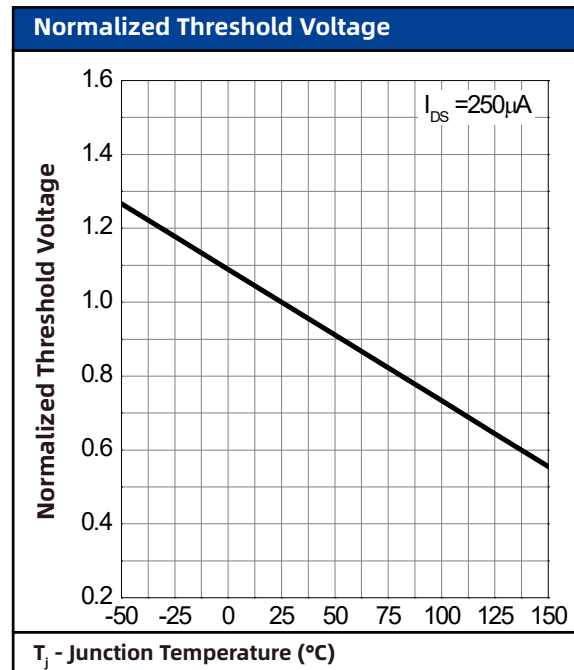
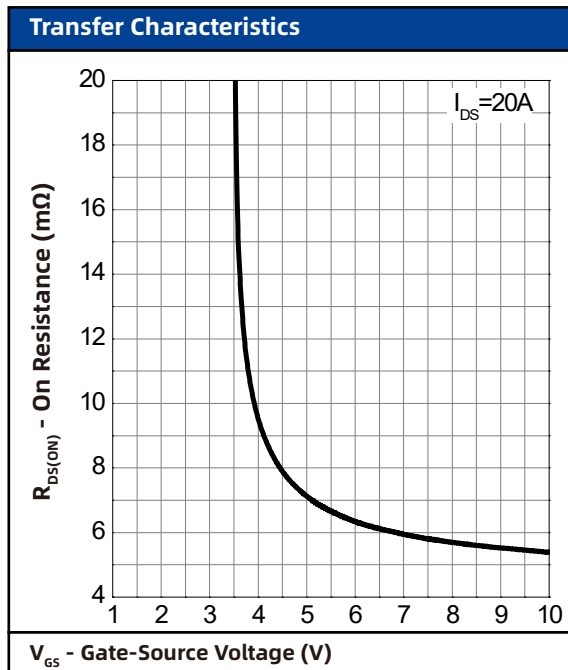
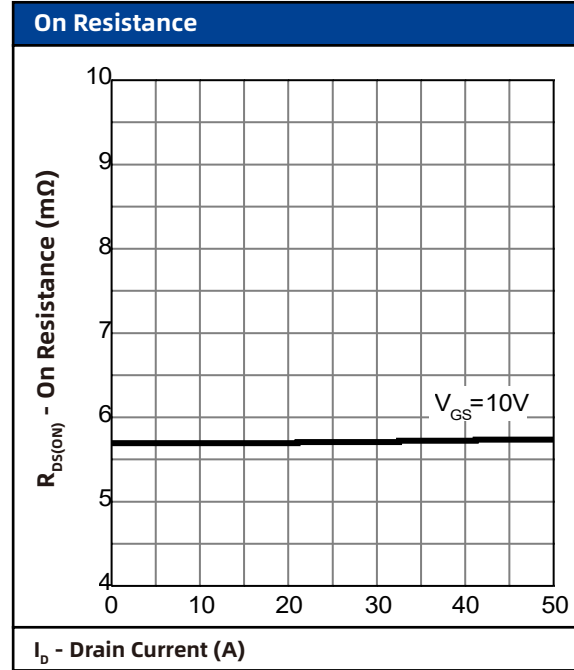
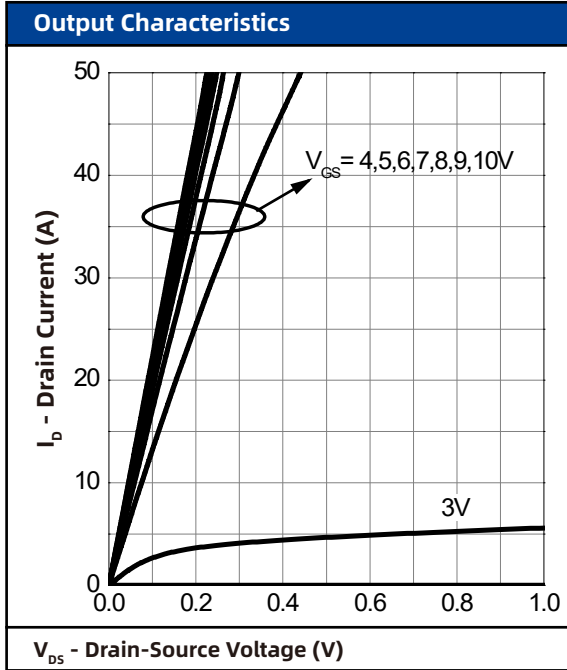
a : Pulse test ; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$

b : Guaranteed by design, not subject to production testing

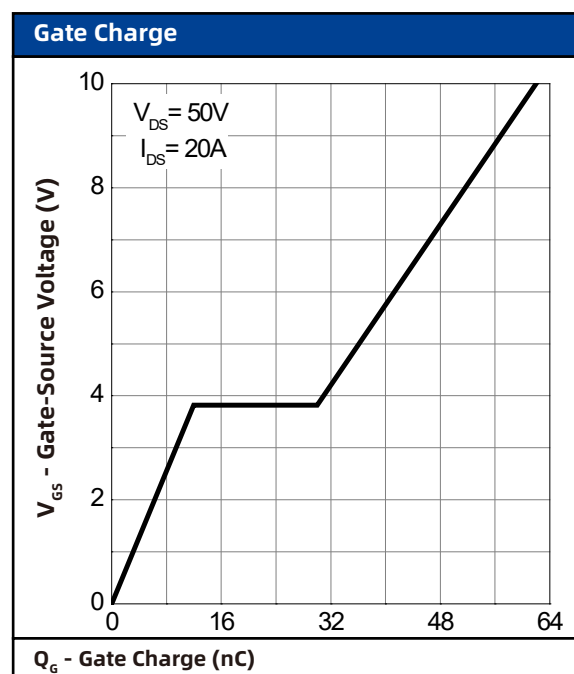
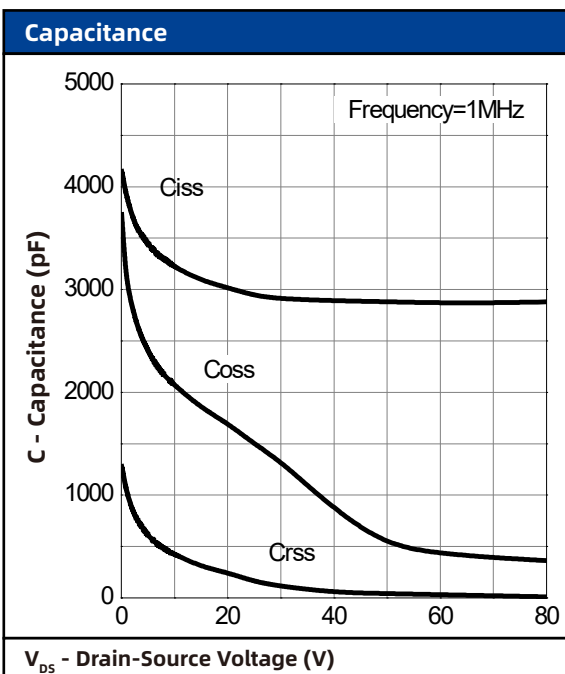
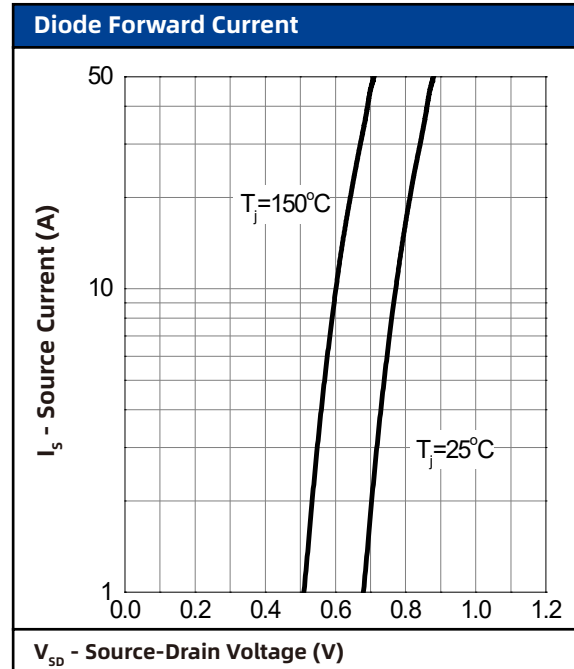
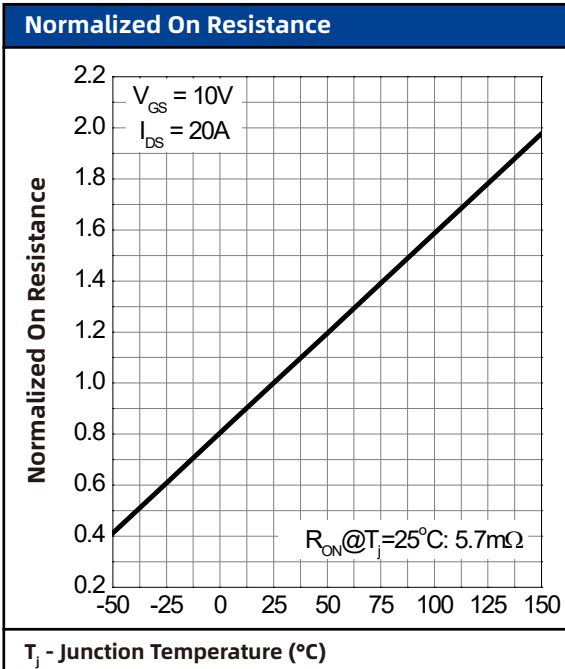
7. Typical Characteristics



7. Typical Characteristics (cont.)



7. Typical Characteristics (cont.)



8. Package Dimensions

PDFN5*6-8L Package

