

## -100V P-Channel Enhancement Mode MOSFET

### 1. Product Information

#### 1.1 Features

- ◇ Advanced TRENCH cell design
- ◇ Low Gate Charge
- ◇ Low On-Resistance
- ◇ RoHS and Halogen-Free Compliant
- ◇ 100%  $\Delta V_{DS}$  & UIS & Rg Tested

#### 1.2 Applications

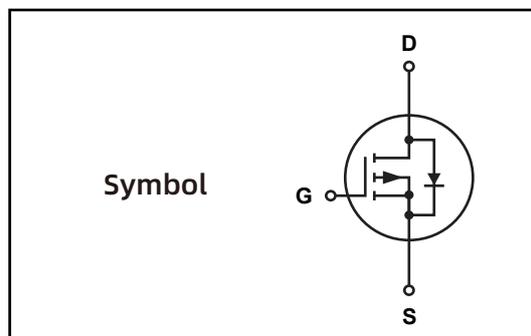
- ◇ DC-DC Converter
- ◇ Drones
- ◇ Motor drivers
- ◇ Light electric vehicles

#### 1.3 Quick reference

- ◇  $BV \cong -100\text{ V}$
- ◇  $P_{\text{tot}} \cong 520\text{ W}$
- ◇  $I_D \cong -133\text{ A}$
- ◇  $R_{DS(\text{ON})} \cong 17\text{ m}\Omega @ V_{GS} = -10\text{ V}$
- ◇  $R_{DS(\text{ON})} \cong 19.5\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$



### 2. Pin Description



### 3.Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit	Note
$V_{DS}$	Drain-Source Voltage	$T_c = 25\text{ }^\circ\text{C}$	-	-100	V	-
$V_{GS}$	Gate-Source Voltage	$T_c = 25\text{ }^\circ\text{C}$	-	$\pm 20$	V	-
$I_D^*$	Drain Current ( DC )	$T_c = 25\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-133	A	Fig.2
		$T_c = 100\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-84	A	
$I_{DM}^{**},^{***}$	Drain Current ( Pulsed )	$T_c = 25\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-150	A	-
$P_{tot}$	Drain power dissipation	$T_c = 25\text{ }^\circ\text{C}$	-	520	W	Fig.1
		$T_c = 100\text{ }^\circ\text{C}$	-	208	W	
$T_{stg}$	Storage Temperature		-55	150	$^\circ\text{C}$	-
$T_j$	Junction Temperature		-	150	$^\circ\text{C}$	-
$I_s$	Continuous-Source Current	$T_c = 25\text{ }^\circ\text{C}$	-	-133	A	-
$E_{AS}^*$	Single Pulsed Avalanche Energy	$V_{DD} = -100\text{ V}, L = 0.1\text{ mH}$	-	500	mJ	Fig.19

### 4.Thermal Characteristics

$R_{\theta JA}^*$	Thermal Resistance- Junction to Ambient	-	23	$^\circ\text{C}/\text{W}$	Fig.16
$R_{\theta JC}^*$	Thermal Resistance- Junction to Case	-	0.24		

Notes :

- \* Surface Mounted on 1 in<sup>2</sup> pad area,  $t \leq 10\text{ sec}$
- \*\* Pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$
- \*\*\* limited by bonding wire

### 5.Marking Information

Product Name	Package	Reel size	Tape width	Quantity	Note
LN180P100C	TO-220C-3L	TUBE	-	50	

Note: COMTECH defines " Green " as lead-free ( RoHS compliant ) and halogen free ( Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C )

## 6. Electrical Characteristics ( $T_A=25^\circ$ Unless Otherwise Noted )

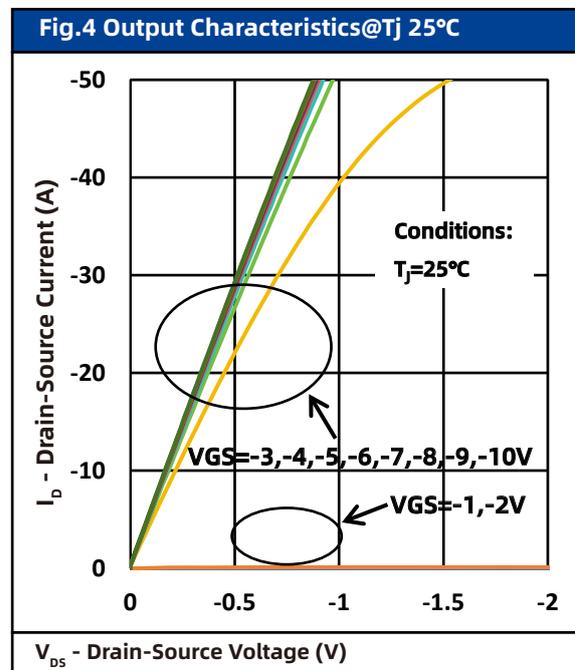
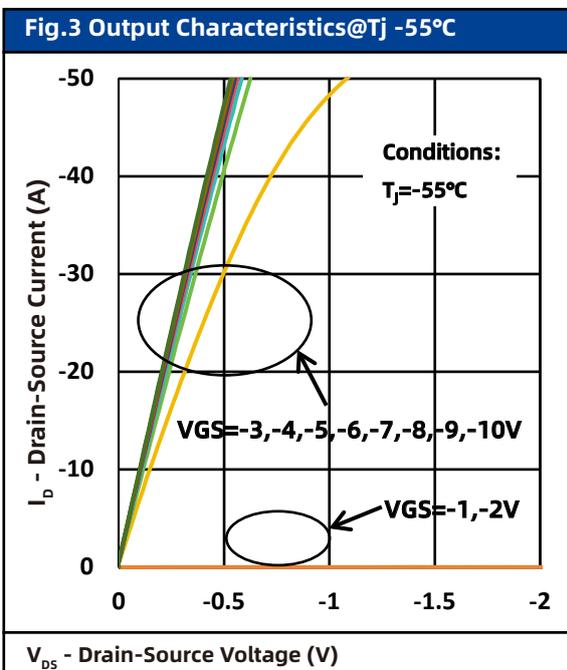
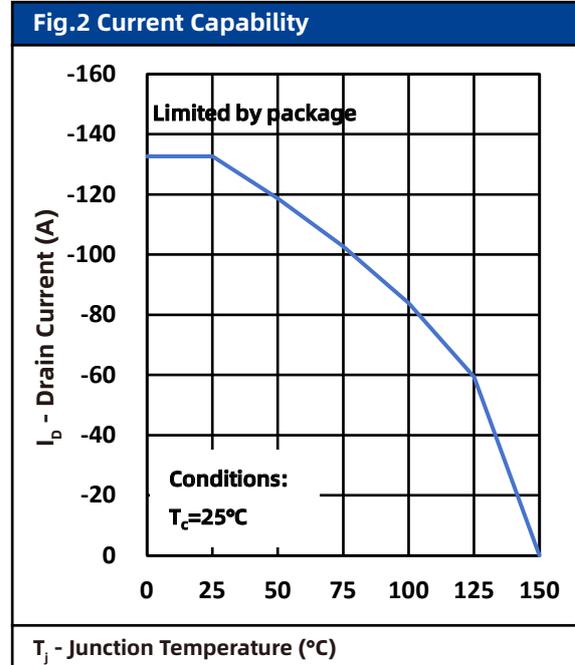
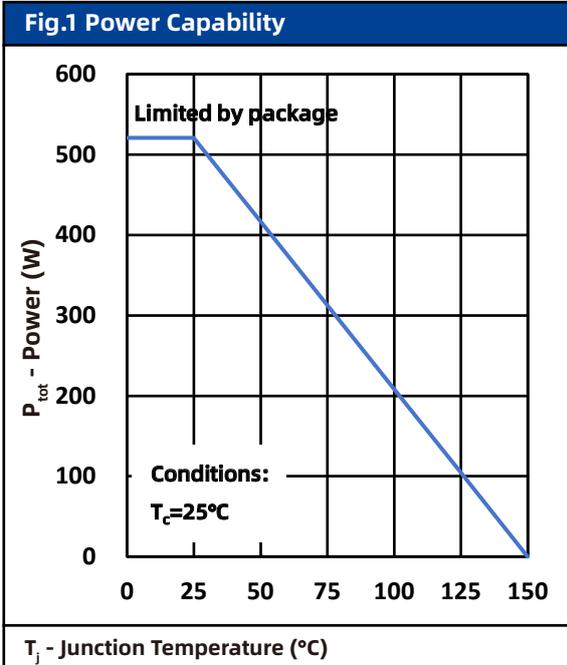
Symbol	Parameter	Conditions	Min	Typ	Max	Unit	Note
<b>Static Characteristics</b>							
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_{DS} = -250\ \mu\text{A}$	-100	-	-	V	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{DS} = -250\ \mu\text{A}$	-1	-	-3	V	
$I_{DSS}$	Drain Leakage Current	$V_{DS} = -100\text{ V}, V_{GS} = 0\text{ V}$	-	-	-1	$\mu\text{A}$	
$I_{GSS}$	Gate Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	-	-	$\pm 100$	nA	
$R_{DS(on)}^a$	On-State Resistance	$V_{GS} = -10\text{ V}, I_{DS} = -5\text{ A}$	-	16	17	m $\Omega$	Fig.8
		$V_{GS} = -4.5\text{ V}, I_{DS} = -5\text{ A}$	-	17.5	19.5		
<b>Diode Characteristics</b>							
$V_{SD}^a$	Diode Forward Voltage	$I_{SD} = -5\text{ A}, V_{GS} = 0\text{ V}$	-	-	-1.3	V	Fig.7
$t_{rr}$	Reverse Recovery Time	$I_{DS} = -5\text{ A}, V_{GS} = 0\text{ V}$	-	36	-	nS	Fig.20
$Q_{rr}$	Reverse Recovery Charge	$di_{SD}/dt = -100\text{ A}/\mu\text{s}$	-	194	-	nC	
<b>Dynamic Characteristics<sup>b</sup></b>							
$C_{ISS}$	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = -50\text{ V}$ Frequency = 1 MHz	-	11519	-	pF	Fig.10
$C_{OSS}$	Output Capacitance		-	304	-		
$C_{rSS}$	Reverse Transfer Capacitance		-	257	-		
$R_G$	Gate Resistance	F= 1 MHz	-	5.5	-	$\Omega$	
$t_d(on)$	Turn-on Delay Time	$V_{DS} = -50\text{ V}, V_{GS} = -10\text{ V},$ $R_G = 2.7\ \Omega, R_L = 20\ \mu\text{H},$ $I_{DS} = -5\text{ A}$	-	15	-	nS	Fig.18
$t_r$	Turn-on Rise Time		-	33	-		
$t_d(off)$	Turn-off Delay Time		-	457	-		
$t_f$	Turn-off Fall Time		-	102	-		
$dv/dt$	Peak Diode Recovery		-	1.2	-		
$di/dt$	Peak Diode Recovery	-	478	-	A/ $\mu\text{s}$		
<b>Gate Charge Characteristics<sup>b</sup></b>							
$Q_g$	Total Gate Charge	$V_{DS} = -50\text{ V}, V_{GS} = -10\text{ V},$ $I_{DS} = -5\text{ A}$	-	225	-	nC	Fig.9
$Q_{gs}$	Gate-Source Charge		-	30	-		
$Q_{gd}$	Gate-Drain Charge		-	20	-		
$V_{plateau}$	Gate plateau voltage		-	2.5	-		

Notes :

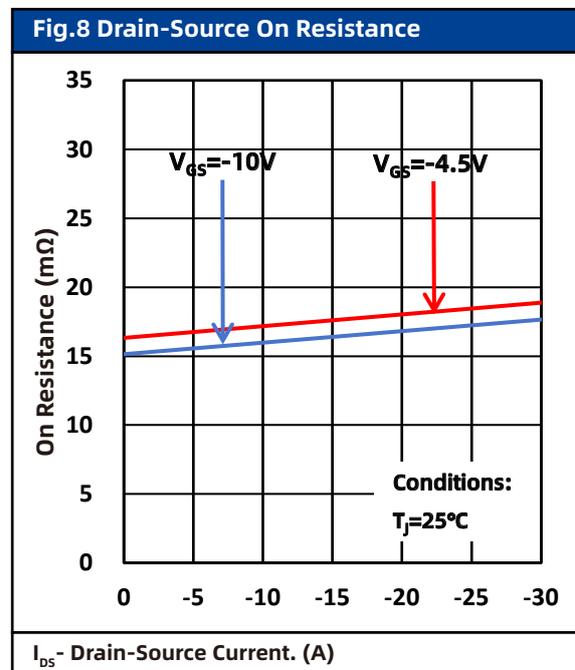
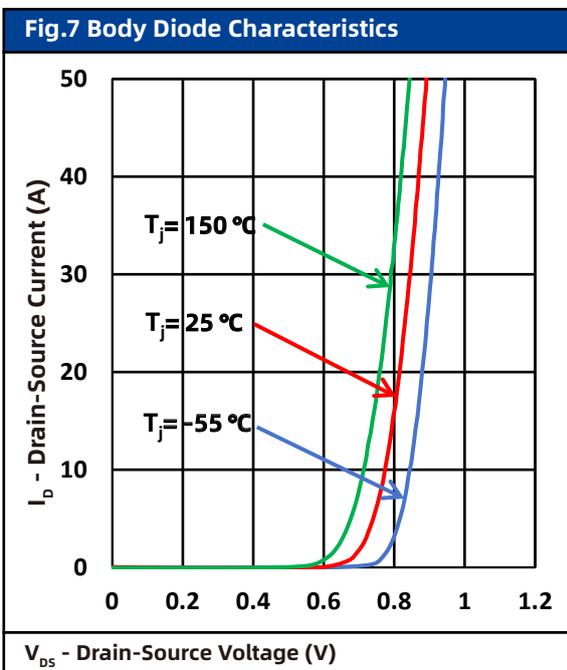
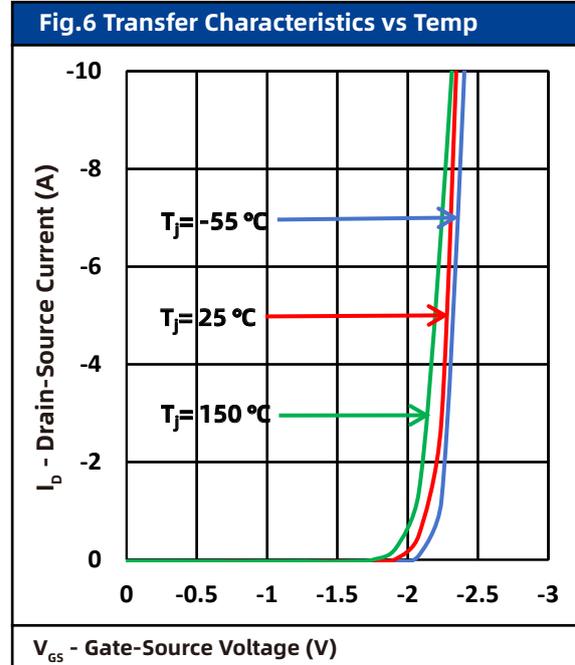
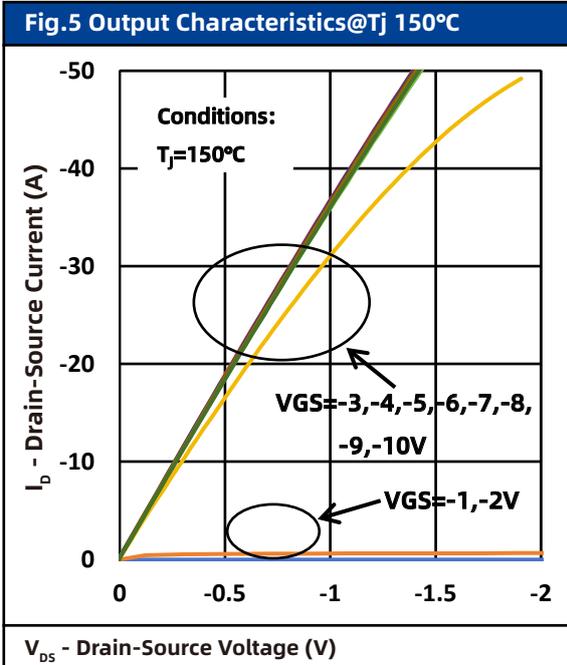
a : Pulse test ; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ 

b : Guaranteed by design, not subject to production testing

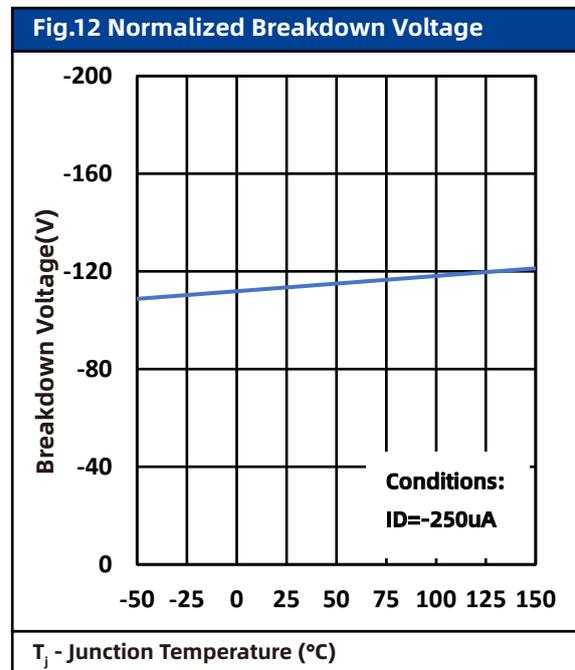
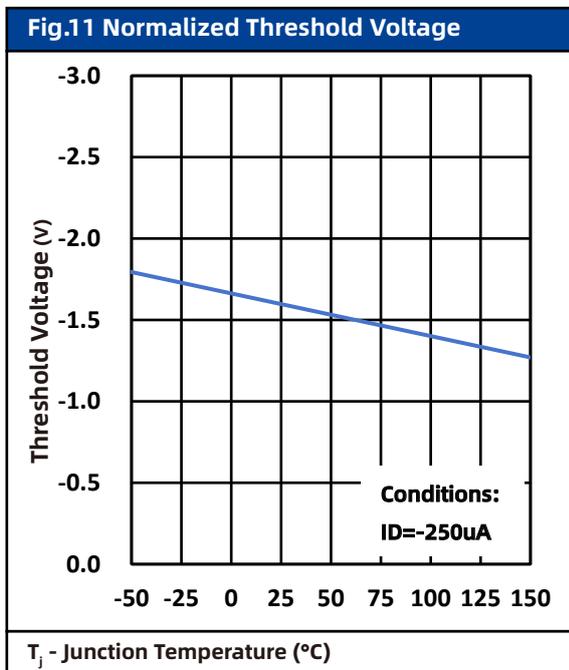
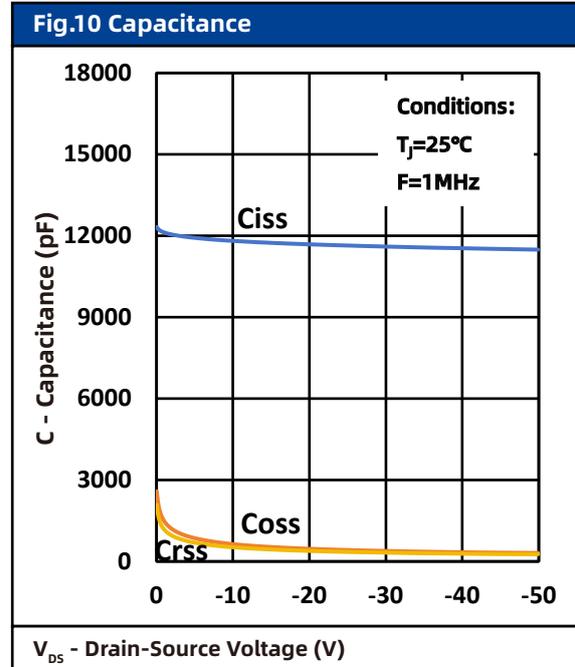
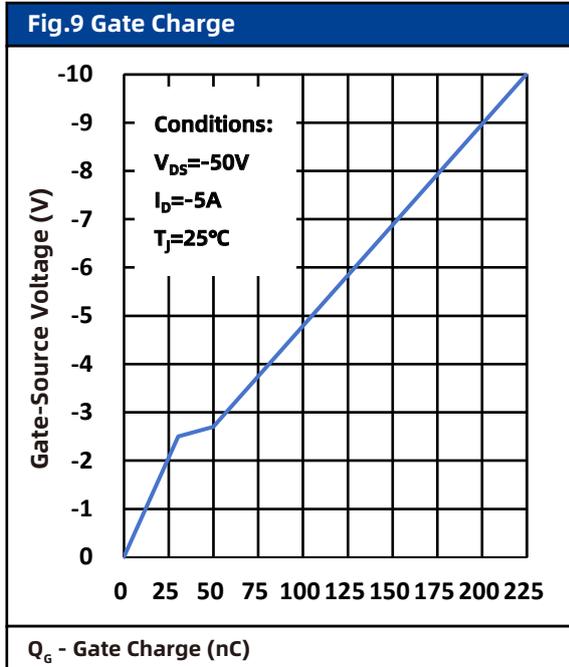
## 7. Typical Characteristics



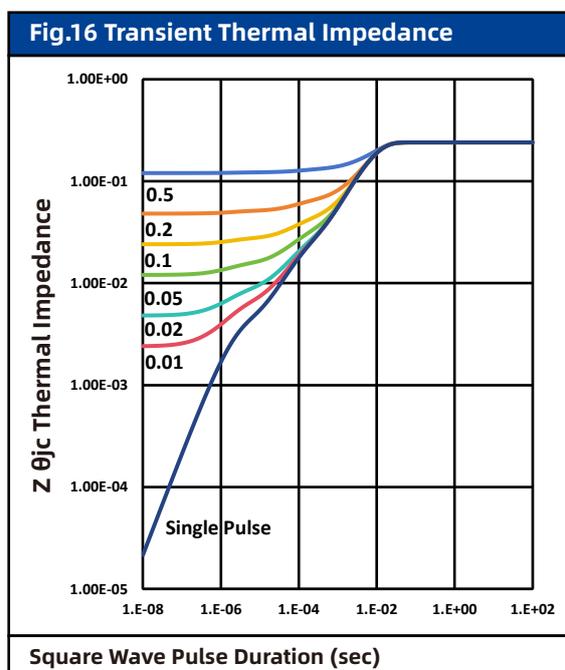
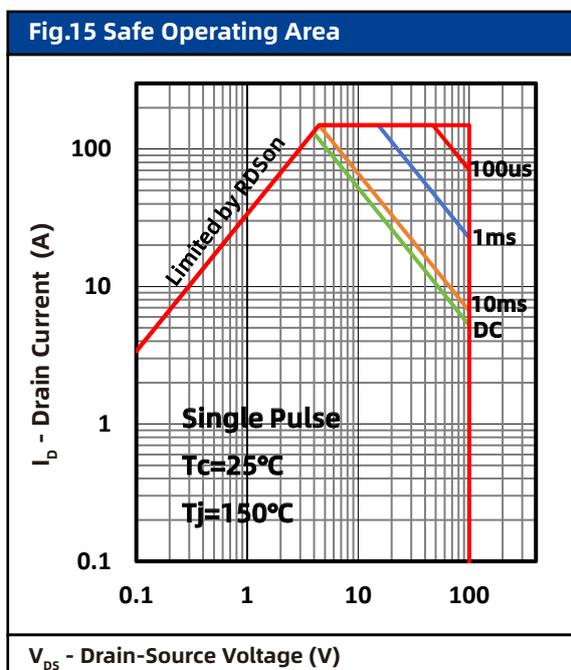
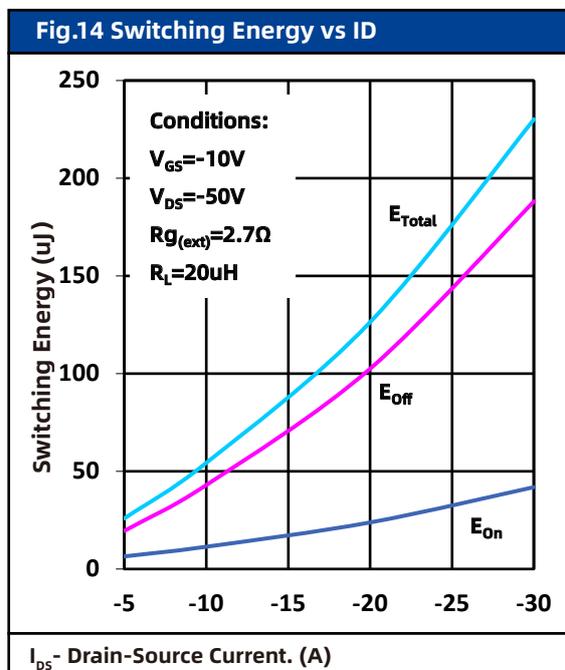
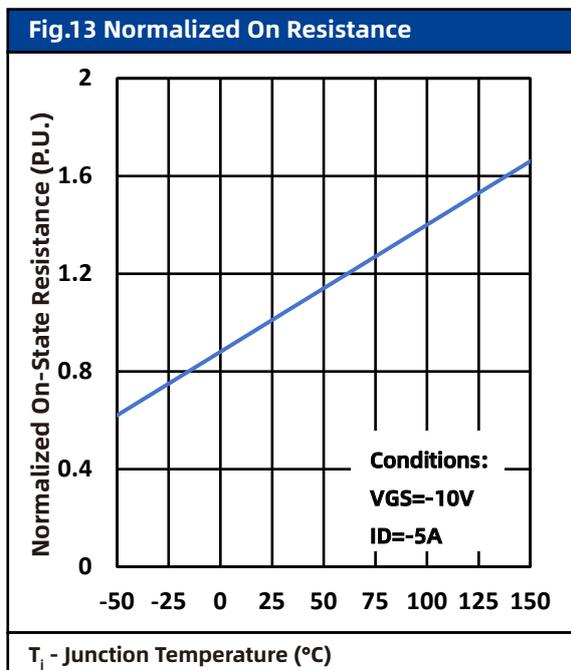
## 7. Typical Characteristics



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Fig.17 Gate Charge Test Circuit & Waveform

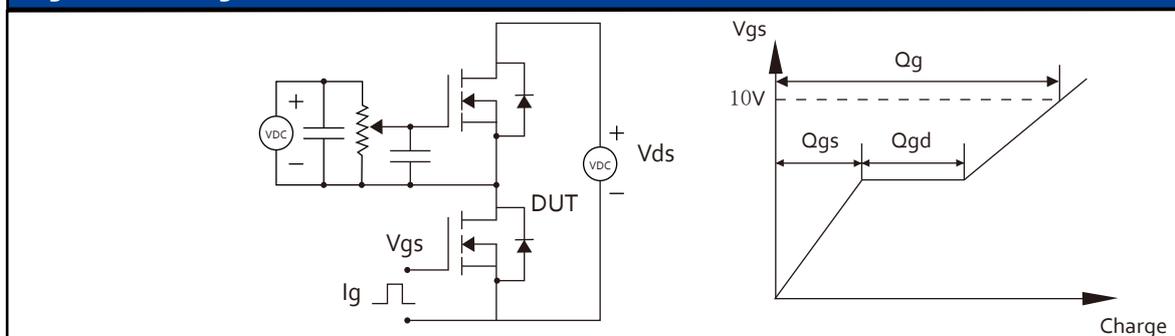


Fig.18 Resistive Switching Test Circuit & Waveforms

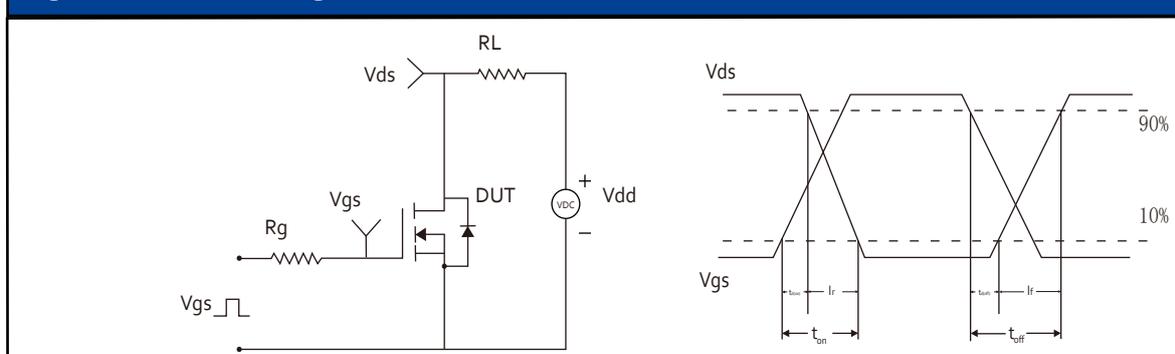


Fig.19 Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

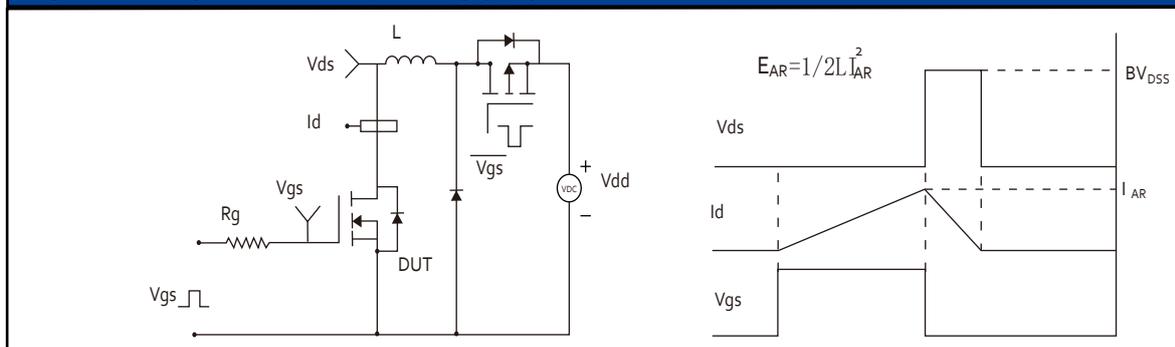
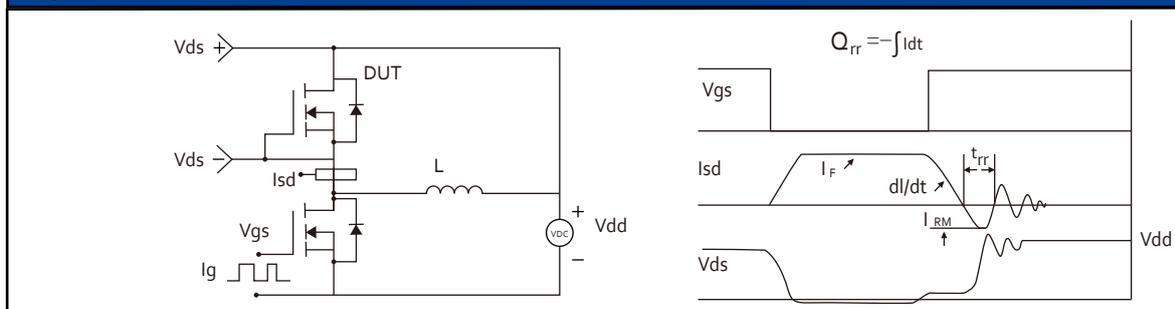
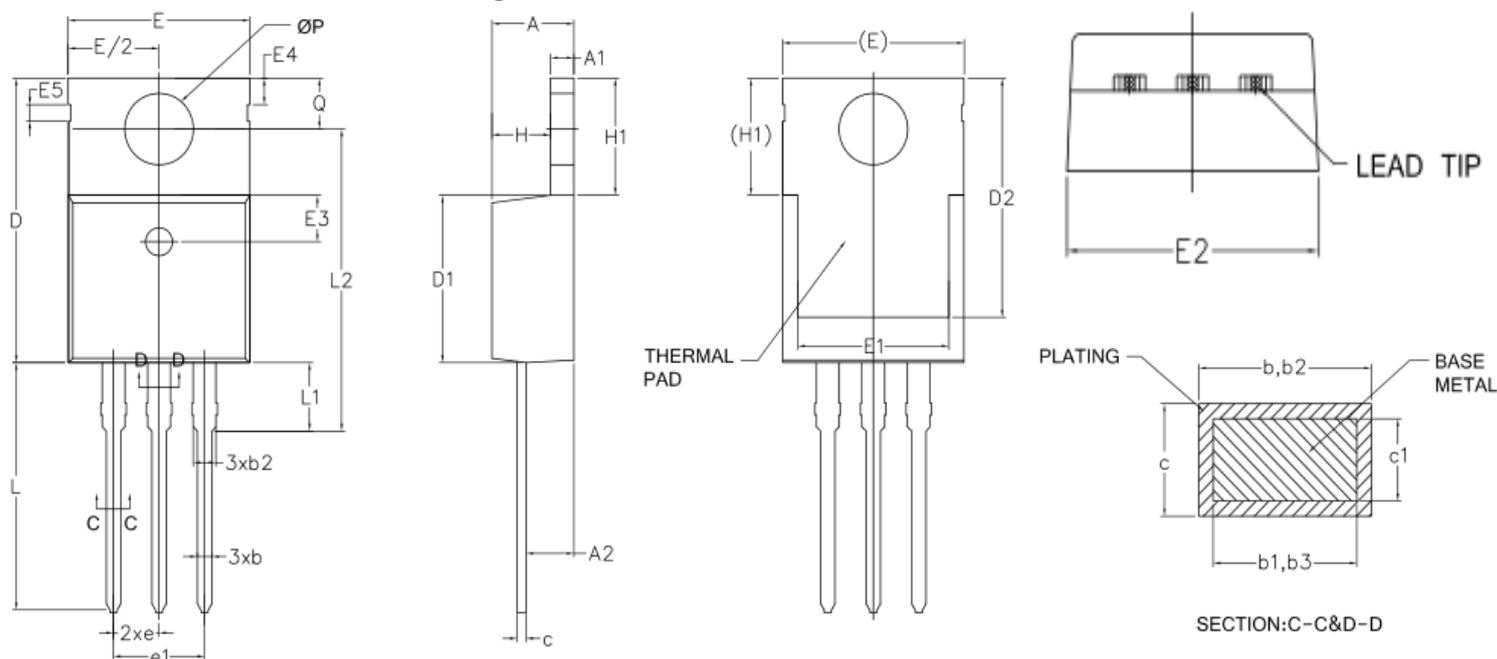


Fig.20 Diode Recovery Test Circuit & Waveforms



## 8. Package Dimensions

### TO-220C-3L Package



Symbol	Dimensions In Millimeters		
	MIN.	NOM.	MAX.
A	4.47	4.57	4.67
A1	1.20	1.30	1.40
A2	2.35	2.67	2.90
b	0.71	0.80	0.91
b1	0.71	0.80	0.86
b2	1.22	1.27	1.36
b3	1.22	1.27	1.31
c	0.47	0.50	0.60
c1	0.47	0.50	0.55
D	14.70	15.30	15.80
D1	8.90	9.00	9.47
D2	11.75	-	13.60
E	9.70	-	10.37
E1	7.00	8.44	8.89
E2	9.80	10.11	10.20
E3	2.40	2.50	2.60
E4	1.27	1.42	1.57
E5		0.90TYP.	
e		2.54BSC.	
e1		5.08BSC.	
H	3.00	3.27	3.40
H1	6.15	6.30	6.45
L	12.90	13.45	14.80
L1	2.54	3.69	3.84
L2	12.13	16.25	16.5
$\Phi P$	3.60	3.84	3.90
Q	2.65	2.74	2.95

## 9. Record of Document amendment

产品名称：LN180P100C  
版权说明：镭诺电子（宁波）有限公司  
联系电话：4008887385

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公司主页：[www.leinuosemi.com](http://www.leinuosemi.com)

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修改记录：  
1.初版发行